



- ◎ **Jitter testing**
- ◎ Multi-task operation at one time
- ◎ Store 20 test results and 9 test configurations, with power-off memories
- ◎ PC operation, store, analysis, print
- ◎ Programmable timer
- ◎ Software updating

Integrated data transmission tester in small unit

- ◎ **Loop delay testing**
- ◎ **Audio testing and monitor**
- ◎ **Auto-Protective Switch testing**
- ◎ **error and alarm testing**
- ◎ **Time-slot analysis**
- ◎ **Line signal frequency testing**
- ◎ **Alarm and histogram analysis**
- ◎ **2M framed unframed testing**

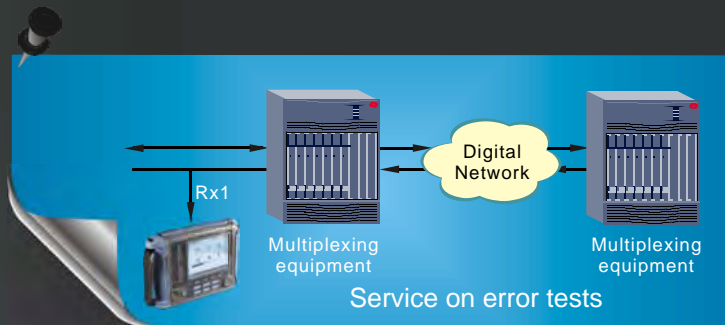
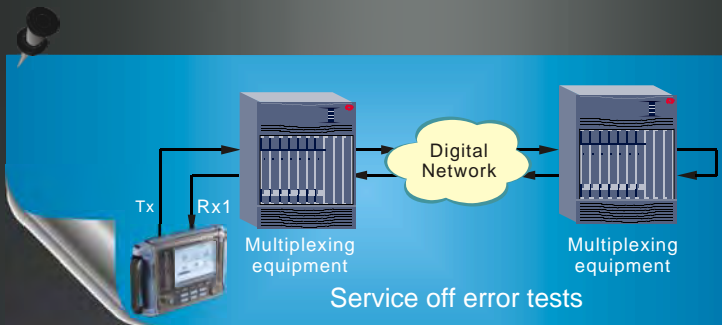
Wildly Used

- ◎ SAT Series E1/Datacom Tester is applicable for transmission project construction inspection, Acceptance and daily maintenance testing.
- ◎ It is used to test access network、transmission network、digital data network etc.

Efficient and Reliable Test Solution

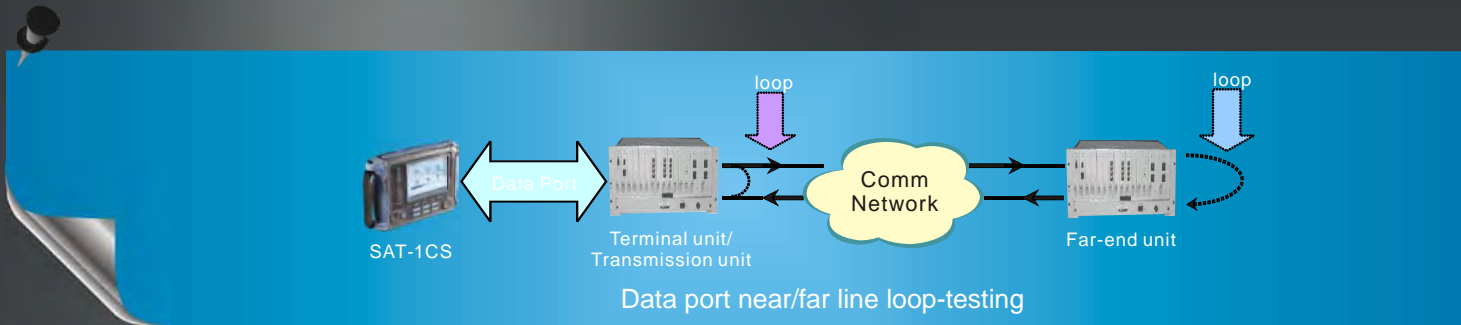
2Mbit/s interface testing

- © Error performance testing of Frame error、CRC error、Code error and E bit error.
- © Error performance analysis of G.821、G.826、M.2100、G.703.
- © 2Mbit/s channel service off error tests, 2Mbit/s channel service on error tests.



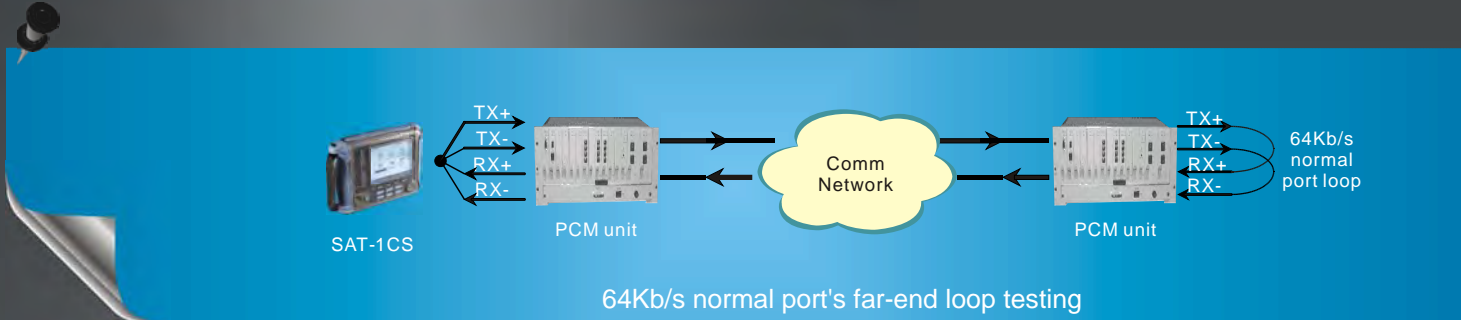
Datacom interface testing

- © V.24/RS232/V.28, V.35, V.36, X.21, RS-449, RS-485, RS422, EIA-530, EIA-530A datacom testing.
- © SYNCH and ASYNCH testing
- © Make use of far end port loop-testing, near end/far end line loop-testing, far end port mutual-testing mode.



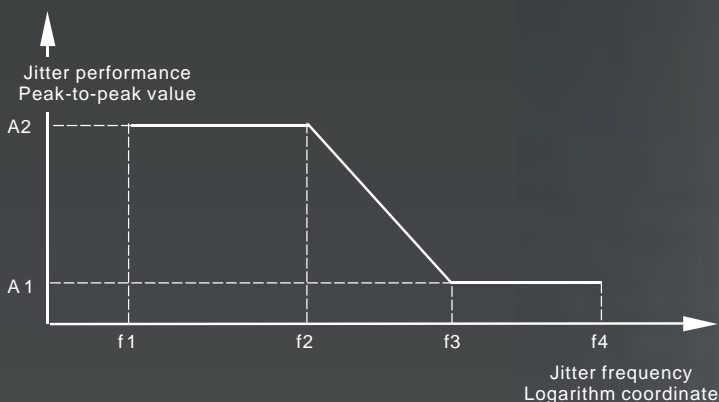
Co-directional 64K bit/s interface testing

- © Make use of 64Kb/s Far end port loop-testing. Near end/far end line loop-testing. 64Kb/s Far end port mutual-testing mode.



Technical Index

	2Mbit/s Technical Index	Co-directional 64k Technical Index	
Signal input rate	2048kbit/s \pm 50ppm (G.703 requirement \pm 100ppm)	64kbit/s \pm 50ppm (G.703 requirement \pm 100ppm)	
Signal cod	HDB3, AMI		
Input jitter	toleranceInput jitter tolerance: Up to G.823.	Up to G.823	
Input balance response	attenuation complies with the law of square root of frequency, and is within the range of 0~6dB at 1024 kHz.		
Input impedance	Unbalance terminating 75 Ω Up to G..703	Input impedance: Balance 120 Ω , up to G.703	
	Balance terminating 120 Ω Up to G..703		
	Reflection loss > 18dB within 50Hz~3100kHz		
	Unbalance bridging: > 750 Ω		Balance bridging: > 1200 Ω
Reflection loss	Unbalancemonitoring:75 Ω ,26dB gain	Balance monitoring:120 Ω , 26dB gain	
	Reflection loss >18dB within 50Hz~3100kHz		
Signal structure	Unframed structure		
Testing pattern	Framed structure:PCM30, PCM31, PCM30CRC, PCM31CRC		
	Framed structure complies with the requirement of G. 704		
Impedance of output interface	Unbalance 75 Ω , up to G. 703	Input impedance: Balance 120 Ω , up to G.703	
External clock	Balance 120 Ω , up to G. 703		
Error code insertion	Signal form :HDB3, NRZ		
	Balance terminating resistance: 120 Ω		
	Balance bridging resistance :75 Ω		
	Unbalance bridging resistance: > 1200 Ω		
Error code insertion	Unbalance bridging resistance: > 750 Ω		
	insertionNone, single, or ratio $10^{-1} \sim 10^{-7}$.		

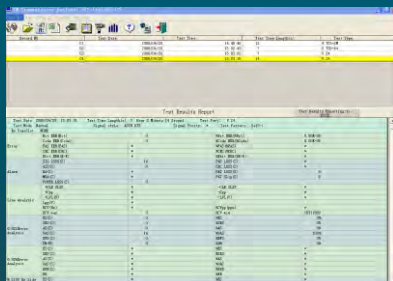


Input Jitter Tolerance(2Mbit/s Technical Index)

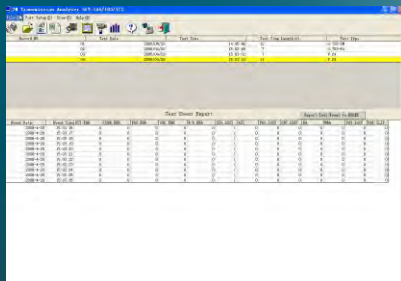


Technical Index

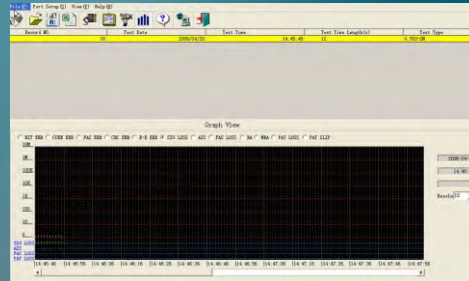
Data interface type		V.24, V.35, V.36, X.21, RS-449, RS-485, EIA-530 and EIA-530A
	Synch mode	Clock source: Internal or external clock
		Phase relation between clock and data: co-direction or reverse direction
		Rate: 1.2, 2.4, 4.8, 9.6, 14.4, 19.2, 38.4, 48, 56(kbps), n×64kbps (n=1~32)
	Asynch mode	Error: ± 15ppm (ppm: parts per million)
		Rate:50,75,110,150,200,300,600,1200,2400,3600,4800,7200,9600;14.4k,19.2k,38.4k,57.6k(bps)
		Data structure: Word length: 5, 6, 7, 8(bits) Stop bit: 1, 2(bits). Odd-even check: odd, even, 1, 0, none.
	Error code insertion	None, single, or ratio 10 ⁻¹ ~10 ⁻⁷ .
Receiver	Synch mode	Clock source: Internal or external clock. Phase relation between receive clock and receive data: Equidirection or reverse direction Clock Rate: 2048kbps at a maximum
	Asynch mode	The rate and data are the same as those of the generator
Testing pattern	2 ⁶ -1, 2 ⁹ -1, 2 ¹¹ -1, 2 ¹⁵ -1, 2 ²⁰ -1, 2 ²³ -1, and artificial code	



Result analysis



Course analysis



Graphs analysis

Specification

Special Power Adapter:

- ◎ Input: AC220V 50Hz
- ◎ Output: DC 9V 1.2A
- ◎ rechargeable battery.
- 4000mAh,6V nickel-hydrogen
- ◎ Working time: 8 hours
- ◎ Charging Time: 8 hours at power-off

Dimension and Weight:

- ◎ L×W×H: 200×162×48mm
- ◎ Weight: 950g

Ambient Parameters:

- ◎ Operation temperature: -10~+50℃
- ◎ Storage temperature:-30~+70℃
- ◎ Humidity:5%~90%,non-condensing



A301 Qinfeng Pavilion, Xi'an Software Park,
No.68, 2nd Keji Road, Xi'an, Shaanxi, P.R.China 710075
Phone: +86 (29)87669443
Fax: +86 (29)87669645
Email: sales@aitelong.com

ISO9001:2008